

GOVERNMENT POLYTECHNIC, MADHUBANI

At – ARARIA SANGRAM, JHANCHARPUR (BIHAR)

P.O. – TULAPATGANJ, PIN 84710



Ref. no- 586

Date: 29/09/26

PRESS RELEASE

Webinar on “Future Scope of Electronics Engineering in VLSI” Organised at Government Polytechnic, Madhubani

Madhubani, 29 September 2026: The **Electronics Engineering Department** of Government Polytechnic, Madhubani, organised a technical webinar on “**Future Scope of Electronics Engineering in VLSI**” for 3rd and 5th semester Diploma students.

The session was delivered by **Ms. Kalpana Sharma**, currently working as a Lead Product Validation Engineer at **Cadence Design Systems India Private Limited**, with an annual CTC of ₹51 lakh. She shared valuable industry insights on **VLSI (Very Large-Scale Integration)**, its applications, emerging opportunities in the semiconductor industry, and career pathways for Electronics Engineering students.

She discussed key domains such as **VLSI Design, RTL Design, Design Verification, Physical Design, Semiconductor Testing and Product Validation**. She also guided students on essential technical skills, including **Digital Electronics, CMOS Technology, Semiconductor Devices, Verilog/SystemVerilog, programming, practical projects and industry-oriented training**. She explained how students can strengthen their technical foundation and prepare for careers in reputed semiconductor and technology companies.

Faculty members of EC DEPT . **Prof. Baidyanath Mandal, Prof. Ashish Kumar Jha, Prof. Kumari Shambhavi and Prof. Vijay Kumar Mahto** also shared their valuable insights on technical skill development, career planning and industry requirements.

The webinar provided students with valuable industry exposure and career guidance. Such expert interactions are beneficial for students’ professional development and the **Training & Placement Cell**, enabling them to understand corporate expectations and prepare effectively for upcoming placement drives, particularly with reputed MNCs.

The department remains committed to organising such **technical webinars** and **seminars** to enhance students’ employability, industry readiness and career prospects.

(182)
29/09/2026
HOD

Department of Electronics



PA
29.09.26
Principal

Government Polytechnic Madhubani

Principal
Govt. Polytechnic
Madhubani



GOVERNMENT POLYTECHNIC MADHUBANI

DEPARTMENT OF SCIENCE, TECHNOLOGY AND TECHNICAL EDUCATION



Future Scope of **ELECTRONICS** in **VLSI**

SMALLER CHIPS | SMARTER SYSTEMS | A BRIGHTER FUTURE





Innovate
Design
Build
The Future

DATE
29 SEP 2026
(TUESDAY)

TIME
11:00 AM

VENUE
TPO CELL

ORGANIZED BY
EC DEPARTMENT

SPEAKER:
KALPNA SHARMA
(Lead Product Validation Engineer
Cadence Design System India Pvt. Ltd.)

GLIMPSES OF WEBINAR CONDUCTED BY EC DEPT ON FUTURE SCOPE OF EC IN VLSI





FROM CLASSROOM CONCEPTS TO THE WORLD OF VLSI



Mahind Bar, Bihar, India

Gp, Mahind Bar, Bihar 847109, India

Lat 26.311547° Long 86.354142°

Tuesday, 29/09/2026 12:55 PM GMT +05:30



GPS Map Camera



EC Department Faculties Prof. Baidyanath Mandal ,Prof .Ashish Kumar Jha & Prof . Kumari Shambhavi With 3rd Sem & 5th Sem EC Students. .



Webinar : Presentation By
Ms. Kalpna Sharma Lead Product Validation
Engineer @
CADENCE DESIGN SYSTEM INDIA PVT LTD